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INDUSTRIAL CO., LTD.
1006, Oaza Kadoma
Kadoma-shi,
Osaka-fu, 571 (JP)**(72) Inventor: **Hayashi, Kenichiro
4-10-207, Ikuno
Katano-shi,
Osaka (JP)
Inventor: Kageyama, Sadashi
5-29-C-302, Akashia-dai
Sanda-shi,
Hyogo-ken (JP)
Inventor: Handa, Kouji
Yodogawa-ryo, 1-4-40, Nonaka-minami,
Yodogawa-ku****Osaka-shi,
Osaka (JP)
Inventor: Uwabata, Hideyo
5-5-C27-307, Korigaoka
Hirakata-shi,****Osaka (JP)
Inventor: Kisoda, Akira
3-4-3, Kaji-machi
Moriguchi-shi,
Osaka (JP)
Inventor: Hayashi, Takaya
1-13-4, Nozoe****Nagaokakyo-shi,
Kyoto (JP)
Inventor: Ogata, Yasuyo
2-12-35, Mishimaoka
Ibaraki-shi,
Osaka (JP)
Inventor: Yasumoto, Yoshio
2-2-1, Syoyo-dai
Nara-shi,
Nara-ken (JP)**(74) Representative: **Kügele, Bernhard et al
NOVAPAT-CABINET CHEREAU,
9, Rue du Valais
CH-1202 Genève (CH)**(54) **Signal processing apparatus and method for wide-screen television system.**

(57) The television signal processing apparatus receives a video signal including a main signal (y,c) and a helper signal (VT,VH,AH), and produces a signal for display based on the main signal and the helper signal, the main signal being arranged in a predetermined portion of the screen having a first aspect ratio and representing a picture having a second aspect ratio, and the helper signal being used for improving the quality of picture of the main signal. The television signal processing apparatus includes: a noise/ghost detection circuit for detecting the level of at least one of noise and ghost included

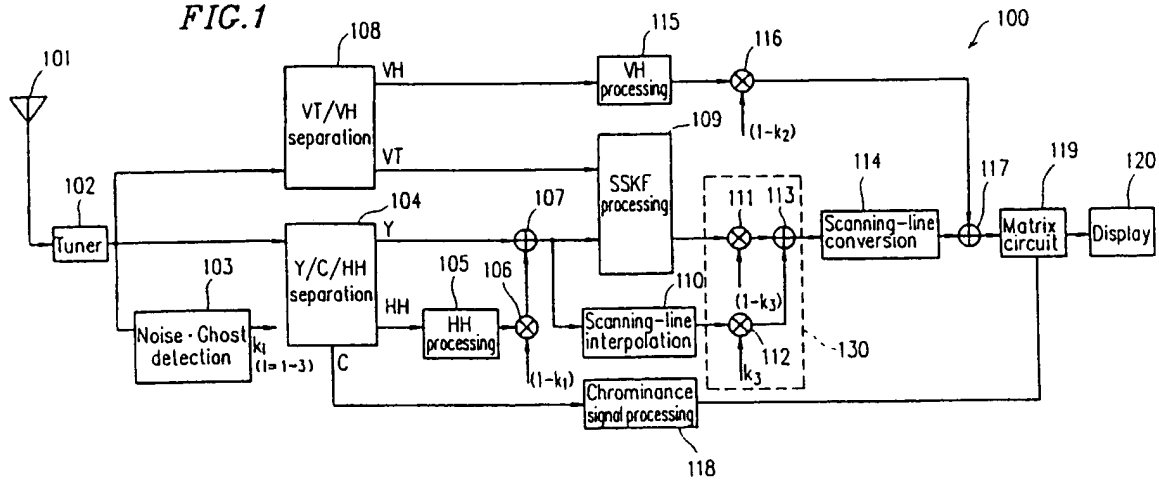
in the video signal, and for producing at least one control signal (ki) based on the detected level; a first signal processing circuit for receiving the main signal and for producing a first reproduction signal by processing the main signal; a second signal processing circuit for receiving the main signal and the helper signal, and for producing a second reproduction signal by processing the main signal and the helper signal; a mixing circuit (113) for receiving the control signal and the first and second reproduction signals, and for adding the first and second reproduction signal with respective weights in accor-

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dance with the control signal; and a circuit for converting an output signal of the mixing circuit into the signal for display. If the video signal includes noise

and/or ghost, the image quality is prevented from being degraded due to the addition of the helper signal.

FIG. 1





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DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
A	GB-A-2 264 418 (GEN ELECTRIC) 25 August 1993 * page 1, line 1 - page 3, line 31 * * page 9, line 15 - page 10, line 15; claims 1,2; figures 2,3 * ---	1,2,5,6, 8,10,11, 14,15	H04N5/44 H04N11/00 H04N5/21
A	US-A-5 067 018 (SAKAMOTO NORIYA ET AL) 19 November 1991 * column 2, line 56 - column 3, line 58 * ---	1,2,10, 11	
A	WO-A-90 09081 (MASSACHUSETTS INSTITUTE OF TECHNOLOGY) 9 August 1990 * page 19, line 10 - page 20, line 20; figure 4 * * abstract * ---	1,2,5,6, 8,10,11, 14,15,17	
D,A	ITE TECHNICAL REPORT, vol. 17, no. 65, October 1993 pages 19-24, KAZUO ISHIKURA ET AL. 'Experimental Hardware of Letterbox EDTV with multiplexed LD/VH helper signals.' * the whole document * -----	1,3,5-8, 10,12, 14-17	TECHNICAL FIELDS SEARCHED (Int.Cl.6) H04N
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 18 July 1995	Examiner Fuchs, P
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons ----- & : member of the same patent family, corresponding document	

